

EPD9186 is 8-bit (256 Gray-Scales) source driver for TFT LCD with 966/960/864/726/720 channels. It provides direct drive, and full dynamic range to drive 16, 777, 216 color TFT displays. Using LBR, users can get a benefit of easy LCD wiring. EPD9186 has a maximum clock frequency of 560MHz, and uses a logic voltage of 2.3V~3.6V. An adoption of Mini Low Voltage Differential Signaling (Mini-LVDS) interface could realize very low EMI and the reduction of the number of input ports.

Features

- Source driver for active-matrix LCD
- 966/960/726/720/864 LCD driving output channels
- VGMA1 ~ VGMA18 for adjusting Gamma correction with 8-bit resolution / 256 gray scale
- Output dynamic range: 0.2V ~ VDDA-0.2V
- Mini-LVDS input interface: Mini Low Voltage Differential Signaling
 - Mini-LVDS input interface for low EMI
 - Minimum mini-LVDS input swing level (CLKP, CLKN, LVxP, LVxN) is 100mV
 - Maximum Bandwidth: 560MHz (VID=150mV), 410MHz (VID=100mV)
- Function
 - H-1Dot (and H-2Dot) and N-line inversion display function provided
 - Cascade function with bi-direction shift control
 - Offset canceling function
 - Adoption of LBR port for easy LCD wiring
 - Repair amplifiers (2ea)
 - Abnormal display prevention mode during power on sequence
 - In rush current protection scheme during reverse power on/off sequence
- Power supply voltage
 - Power for logic circuit (VDDD): 2.3V~3.6V
 - Power of LCD driving voltage (VDDA): 10.0V ~ 18V
- COF packaging

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