

EPD9171 is an 8-bit (966/960/726/720 channels) Source Driver for TFT LCD with the Clock Embedded Differential Signal (CEDDS) interface. It delivers a display of 16, 777, 216 colors by 256 of gamma-referenced output values that are controlled by an internal D/A converter and 18 external references. Also, the CEDDS interface is integrated into EPD9171 to improve data transfer speed, reduce EMI effects, and minimize wiring on the PC board. EPD9171 uses Half Analog Power for reducing power consumption & chip surface temperature.

Features

- Source driver for active matrix LCD
- 966/960/726/720 LCD driving output channels (@Pin_SEL 1/0=LH, HH)
- 961/960/721/720 LCD driving output channels (@Pin_SEL 1/0=LL, HL)
- VGMA1 ~ VGMA18 for adjusting Gamma correction with 8-bit resolution / 256 gray scale
- Output dynamic range: 0.2V ~ VDDA - 0.2V
- CEDDS input interface:
 - Pair option: One or Two pair operations are supported.
 - PIN_SEL1=L, 0.7Gbps~1.54Gbps
 - PIN_SEL1=H, 1.2Gbps~3.4Gbps
- Function
 - H-1Dot (and H-2Dot) and N-line inversion display function provided
 - Bi-directional LOCK pad (optional)
 - Data de-scrambling
 - Half VDDA function
 - Adjustable SOE control (ASC) to support different channel mode on the same panel
 - Abnormal display prevention mode during power on sequence
 - In rush current protection scheme during reverse power on sequence
 - GDE
 - Smart TP
 - Equalizer (EQ)
 - Output Buffer power control (PWRC)
- Power ON/OFF Reset Function (XON)
- Power supply voltage
 - Power for logic circuit (VDDD/VDDIF): 1.62V~1.98V
 - Power of LCD driving voltage (VDDA): 12.0V~18.0V
 - Half Power for HVDDA: (1/2 VDDA) +/-1.0V
- COF packaging

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