

Certificate



No.: 968/FSP 2901.00/25

Product tested

Soft Intellectual Property (IP)
RISC-V processor

Certificate holder

Beijing ESWIN Computing
Technology Co., Ltd.
Room 101, 1st Floor,
Building 3, No.18,
Kechuang 10th Street,
BDA (Beijing Economic
Technological
Development Area),
Beijing City,
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Type designation

R500A
For details see actual "Revision List"

Codes and standards

ISO 26262-2:2018
ISO 26262-5:2018

ISO 26262-8:2018
ISO 26262-9:2018

Intended application

The soft Intellectual Property (IP) RISC-V processor R500A, developed as a Safety Element out of Context (SEooC), complies with the applicable requirements of ISO 26262 for ASIL D regarding the avoidance of systematic faults and for ASIL B regarding the detection and control of random faults. It can be used in safety-related applications up to ASIL D.
In addition to the above listed standards ISO 26262-10 and ISO 26262-11 were used to support the evaluation.

Specific requirements

The integrator/user is obliged to validate the assumptions and constraints defined by the manufacturer, under consideration of the instructions of safety manual, user manual and integration manual.

Valid until 2030-05-20

The issue of this certificate is based upon an evaluation in accordance with the Certification Program CERT FSP2 V3.0:2020 in its actual version, whose results are documented in Report No. 968/FSP 2901.00/25 dated 2025-05-09. This certificate is valid only for products, which are identical with the product tested. Issued by the certification body accredited by DAkkS according to DIN EN ISO/IEC 17065. The accreditation is only valid for the scope listed in the annex to the accreditation certificate D-ZE-11052-02-00.

TÜV Rheinland Industrie Service GmbH
Bereich Automation
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Am Grauen Stein, 51105 Köln

Köln, 2025-05-20

Certification Body Safety & Security for Automation & Grid

Dipl.-Ing. (FH) Stefan Goi