

EMM9500 is a monitor scaler IC that supports maximum resolution of QHD 180Hz, suitable for gaming, multiple market segments and display applications.

EMM9500 combines two HDMI 2.0 compliant digital input interfaces with HDCP1.4/HDCP2.2, and two DP 1.4 digital input interfaces with HDCP 1.4/HDCP 2.2.

EMM9500 has abundant low speed interfaces like I²C, SPI, UART, SARADC, PWM to meet different application scenarios.

EMM9500 has one embedded 32-bit RISC-V CPU. It initializes the peripherals and the video data path, handles instantly the events that come up from the peripherals.

Features

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| CPU | <ul style="list-style-type: none">• 32-bit RISC-V CPU |
| Panel Support | <ul style="list-style-type: none">• QHD 180Hz & UHD 60Hz |
| RX | <ul style="list-style-type: none">• DP 1.4*2• HDMI 2.0*2 |
| TX | <ul style="list-style-type: none">• eDP 8lane |
| PQ | <ul style="list-style-type: none">• Support FOD• Support Freesync• Support HDR10• Support AI-Light• Support Gamma• Support Uniformity• Support local Dimming• Support PIP/PBP• Support MPRT• Support 3D LUT |
| Others | <ul style="list-style-type: none">• Support Font OSD• Support Headphone & I²S & SPDIF• Support SARADC• Support I²C/SPI/PWM/GPIO• Embedded DRAM |
| Packaging | <ul style="list-style-type: none">• LQFP256 PIN |

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